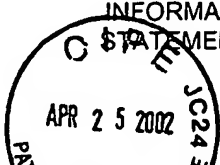


FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANTS 	ATTY. DOCKET NO. 174/211	APPLICATION NO. 10/059,014
	APPLICANTS Chong Lee et al.	CONFIRMATION NO. 5565
	FILING DATE January 29, 2002	GROUP 2819

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
T.W.	3,473,160	10.14.69	Wahlstrom			
T.W.	5,689,195	11.18.97	Cliff et al.	326	41	
T.W.	5,909,126	06.01.99	Cliff et al.	326	41	
T.W.	6,215,326	04.10.01	Jefferson et al.	326	41	
T.W.	U.S. Patent Appl. No. 09/516,921		Ngai et al.			03/02/00

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
T.W.	"Lucent Introduces 10Gb/s Ethernet FPGAs," <u>Programmable Logic News and Views</u> , November 2000 (Vol. IX, No. 11), pp. 7-8.
T.W.	<u>Protocol Independent Gigabit Backplane Transceiver Using Lucent ORT4622/ORT8850 FPSCs</u> , Application Note, June 2000, Lucent Technologies Inc., pp. 1-10.
T.W.	<u>ORCA® ORT82G5 0.622/1.0 - 1.25/2.0 - 2.5/3.125 Gbits/s Backplane Interface FPSC</u> , Product Brief, February 2001, Lucent Technologies Inc., pp. 1-8
T.W.	<u>ORCA® ORT82G5 1.0 - 1.25/2.0 - 2.5/3.125 Gbits/s Backplane Interface FPSC</u> , Preliminary Data Sheet, July 2001, Agere Systems Inc., pp. 1-35.
T.W.	<u>ORCA® ORT8850 Field-Programmable System Chip (FPSC) Eight-Channel x 850 Mbits/s Backplane Transceiver</u> , Product Brief, July 2001, Agere Systems Inc., pp. 1-6.
T.W.	<u>ORCA® ORT8850 Field-Programmable System Chip (FPSC) Eight-Channel x 850 Mbits/s Backplane Transceiver</u> , Data Sheet, July 2001, Agere Systems Inc., pp. 1-36.
T.W.	<u>Rocket I/O™ Transceiver User Guide</u> , v1.2 (Feb. 25, 2002) < http://www.xilinx.com/publications/products/v2pro/userguide/ug024.pdf >.
T.W.	<u>Virtex-II Pro Platform FPGA Handbook</u> , v1.0, pp. 1-6, 27-32, 121-126, and 162-180 (Jan. 31, 2002) < http://www.xilinx.com/publications/products/v2pro/handbook/ug012.pdf >.
T.W.	<u>Virtex-II Pro Platform FPGAs: The Platform for Programmable Systems</u> (visited 03/05/2002) < http://www.xilinx.com/virtex2pro >.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.